

User Guide

N32A455VEL7-EVB Development Board Hardware Usage Guide

Introduction

The purpose of this document is to allow users to quickly familiarize themselves with the N32A455VEL7-EVB development board, understand the functions, usage instructions and precautions, and facilitate MCU debugging and development based on the development board.

Contents

1	<i>Hardware Development Instructions</i>	3
1.1	Brief	3
1.2	Development Board Function	3
1.3	Development board layout	4
1.4	Development Board Jumper Usage Instructions	6
1.5	N32A455VEL7-EVB Development Board Schematic	7
2	<i>Version History</i>	8
3	<i>Disclaimer</i>	9

1 Hardware Development Instructions

1.1 Brief

The N32A455VEL7-EVB development board is used for sample development of 32-bit N32A455VEL7 chips of Nsing Technology. This document describes the functions, usage instructions and precautions of the N32A455VEL7-EVB development board in detail.

1.2 Development Board Function

The part number of the main MCU chip in the development board is N32A455VEL7, and it is packaged with LQFP100 pins. The development board connects all functional interfaces to facilitate customer development, In addition, there are power supply and GND on the board to facilitate debugging.

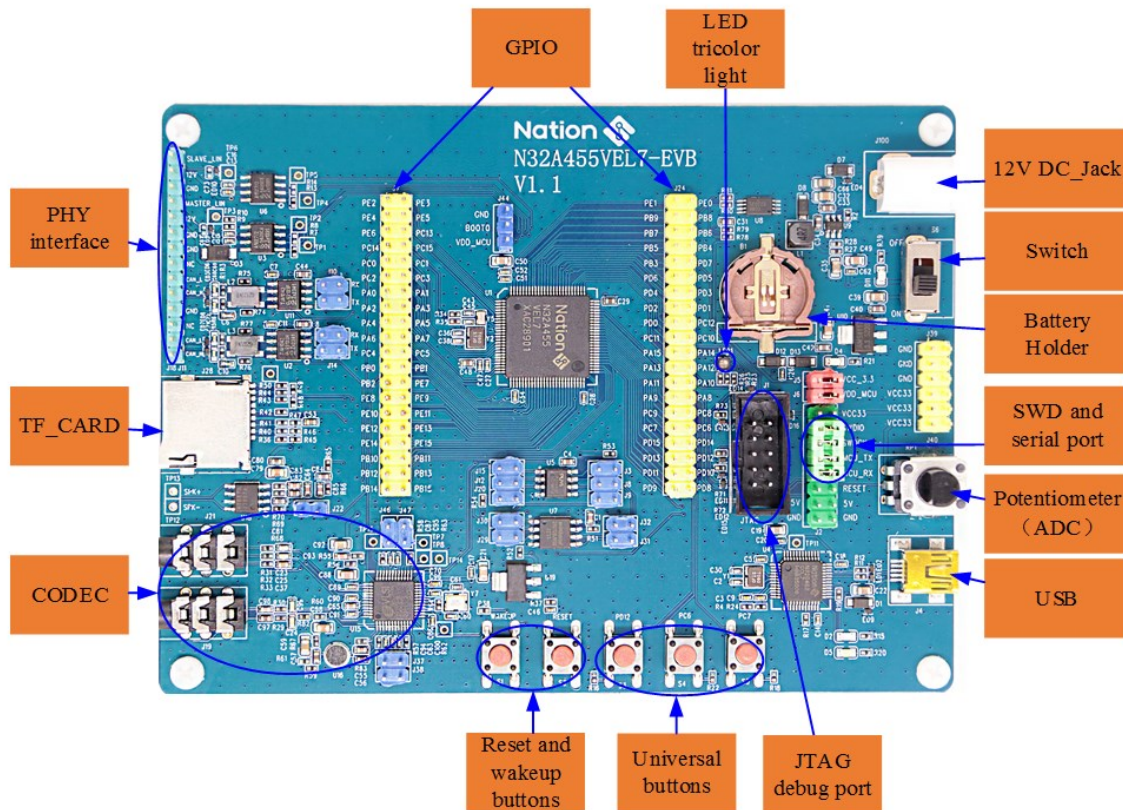
The main functional interfaces are as follows:

Table 1 1 Main Function Interface Description

No.	Interface	Interface Tag	Illustrate
1	USB	J4	USB connector
2	NSLINK_SW D /UART	J2	NSLINK signal
3	JTAG	J1	JTAG debug interface
4	MCU IO	J7、 J24	MCU GPIO pins
5	PHY	J11、 J13、 J16、 J18	PHY communication interface

1.3 Development board layout

Figure 1-1 Development Board Layout



- Power supply for the development board**

The development board has two power supply methods: 1) DC_JACK power supply, input voltage 12V, DC_JACK J100; 2) USB power supply, input voltage 5V, USB J4, both power supply modes are controlled by switch S6. After the switch S6, the power input to the LDO is converted to 3.3V voltage, and then the voltage is divided into two channels, one supports the power supply of the MCU alone, selected by the jumper J6, and the other supports the power supply of the other functional modules, selected by the jumper J5.

- NSLINK interface**

The NSLINK interface can be used for program download and debugging, supporting two download

modes: JTAG and SWD, with a virtual serial port, which can be selected by jumper J2.

- **JTAG interface (J1)**

The development board has an independent 10Pins JTAG/SWD for Jlink debugging.

- **PHY communication interface**

Supports communication for 2-way CAN PHY and 2-way LIN PHY interfaces. When using 2-way CAN PHY for debugging, two sets of jumper caps J10 and J14 need to be connected. The lead-out interfaces for the 2-way CAN PHY are J11 and J18, and the lead-out interfaces for 2-way LIN PHY are J13 and J16.

- **TF_CARD**

Onboard TF card slot, located on the TOP side of the development board, the TF_Card is J28.

- **CODEC**

The development board uses CODEC chip (VS1053B) with input LINE_IN interface J19 and electret MIC U16, one headphone output interface J21, and outputs one external speaker interface SPK1&SPK2 through the power amplifier chip (HT6872). The CODEC chip is U15, and the power amplifier chip (HT6872) is U18. CODEC supports both SPI and I2S communication. If SPI communication is selected, jumpers J46 and J47 need to be connected; if I2S communication is selected, jumpers J37 and J38 need to be connected.

- **EEROM&PSRAM**

The development board uses EEROM chip (CAT24C08YI-GT3), EEROM chip U6.

- **SPI_FLASH&QSPI_FLASH**

The development board features one SPI FLASH (W25Q128JVSQTR) and one QSPI FLASH (P25Q40HA-SSH-IT). The SPI FLASH is U7, and the QSPI FLASH U5, the two are selected by jumpers, if SPI FLASH is selected, connect the jumper J29, J30, J31, J32; if QSPI FLASH is selected, connect jumpers J3, J8, J9, J12, J15, J20.

- **KEY**

There are 5 mechanical buttons on the development board, the WAKEUP button is numbered S1, the RESET button is numbered S2, and the general buttons are numbered S3, S4, and S5.

- **GPIO port**

80 independent GPIO ports, which can be multiplexed with other function pins, see "DS_N32A455 Series Data Sheet" for details.

1.4 Development Board Jumper Usage Instructions

Table 1 2 Description list of key jumpers on the development board

No.	Jumper	Jumper Function	Instructions for Use
1	J5	3.3V power supply option	To supply power to other functional modules except MCU, it must be connected
2	J6	3.3V power supply option	To supply power to the MCU, it must be connected
3	J44	Connect to VDD_MCU to pull up, Connect to GND to pull down	Then pull up on the left and pull down on the right.
4	J2	NSlink jumper selection	Press the silk screen to select the desired download method.
5	J46、J47	CODEC SPI communication selection	Connect this item when CODEC selects SPI communication mode
6	J37、J38	CODEC I2S communication selection	Connect this item when CODEC selects I2S communication mode
7	J29、J30、J31、J32	SPI flash selection	Connect this item when using SPI flash

8	J3、J8、J9、J12、 J15、J20	QSPI flash selection	Connect this item when using QSPI flash
9	J10、J14、	CAN PHY selection	Connect this item when using CAN PHY communication

1.5 N32A455VEL7-EVB Development Board Schematic

Please refer to the PDF file "N32A455VEL7-EVB_V1.1" for the schematic diagram of N32A455VEL7-EVB.

- Description of peripheral devices:**

- 1) VCC_MCU: Put two capacitors near pin VDD_3, 4.7uF and 0.1uF respectively; put two capacitors near pin VDDA, respectively 1uF and 0.1uF, and put 0.1uF capacitors near the other power pins.
- 2) VCC33: Put a 0.1uF capacitor to the ground near the pin.

2 Version History

Version	Date	Changes
V1.0	2023-02-25	Initial version

3 Disclaimer

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