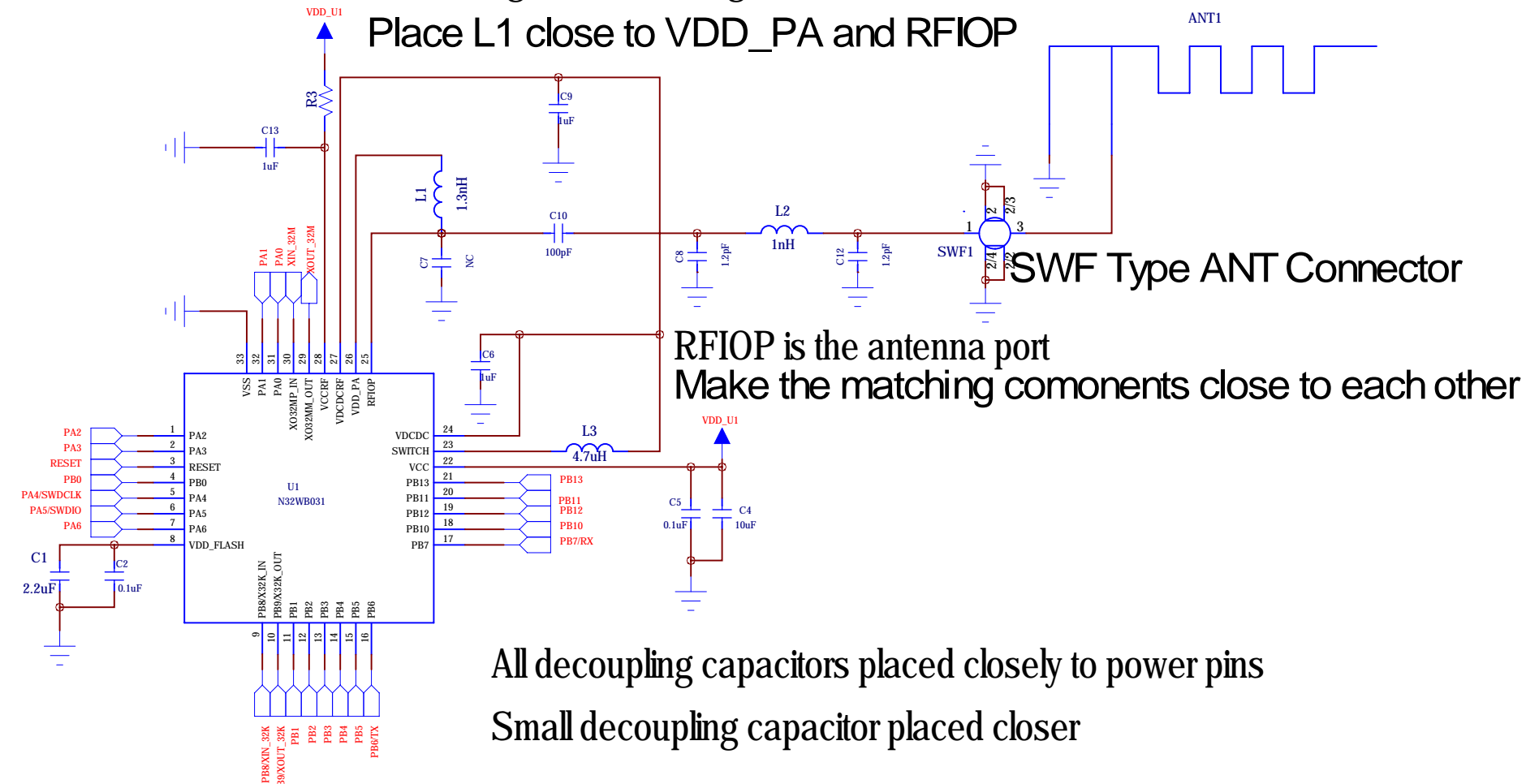


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

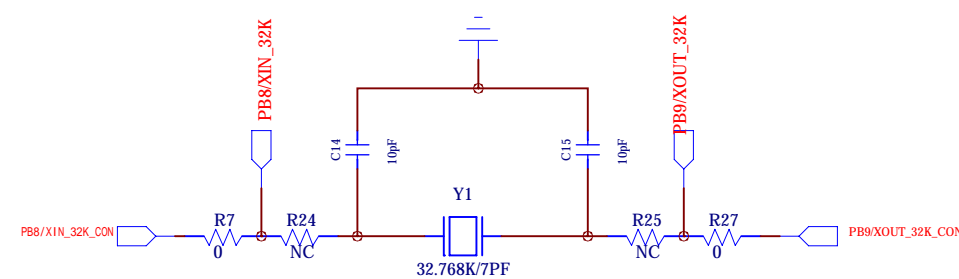
BlueMCU

VDD_PA generates voltage to bias internal PA

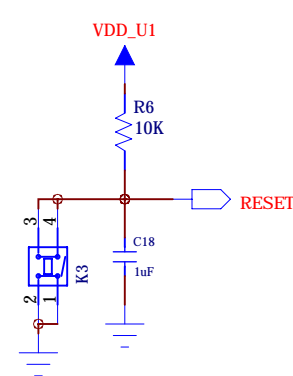
Place L1 close to VDD_PA and RFIOP



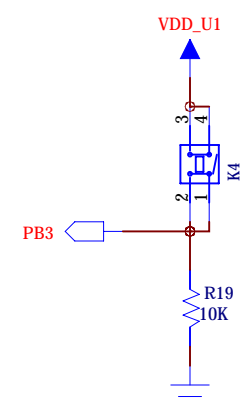
LSE



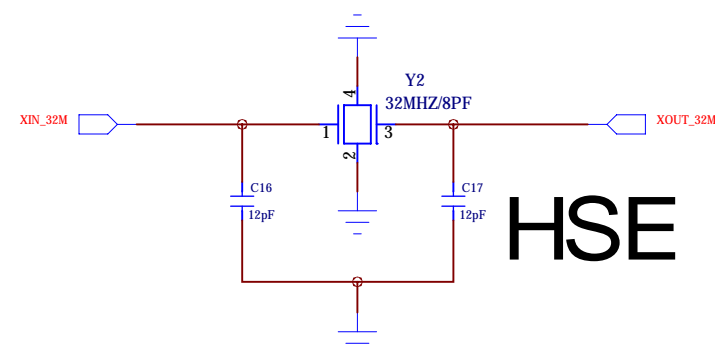
RESET



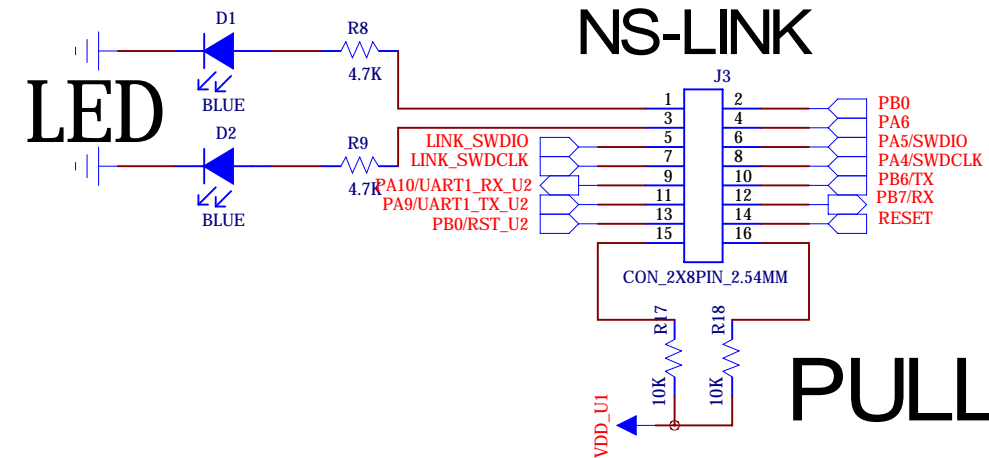
WAKEUP



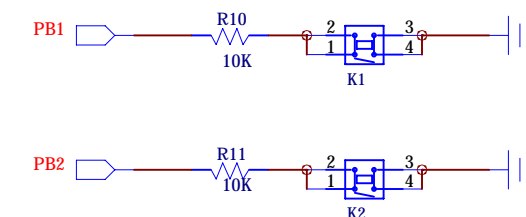
HSE



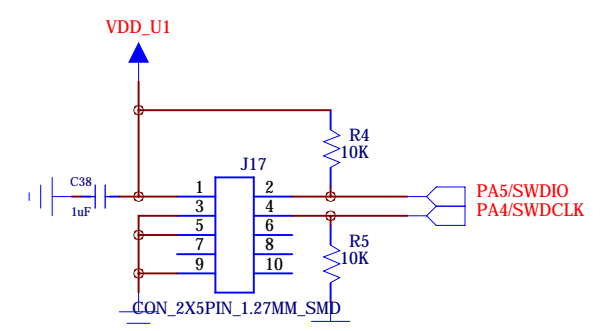
NS-LINK



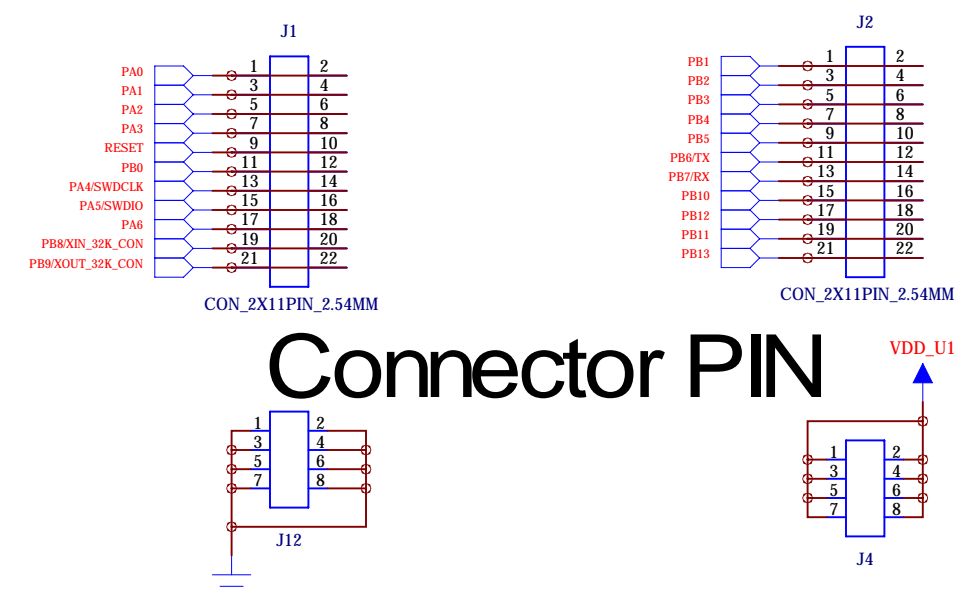
KEY



J-Link Connector



Connector PIN



TITLE: N32WB031_STB	
MODEL NO:	REV: V1.3
CUSTOMER:	DRAWN: Nations Technologies Inc.
SHEET: 1 OF 2	CHKD:
DATED: 2022-09-30	APPD:
COMPANY:	

